

THE SINGLE-ENDED S-MATRIX

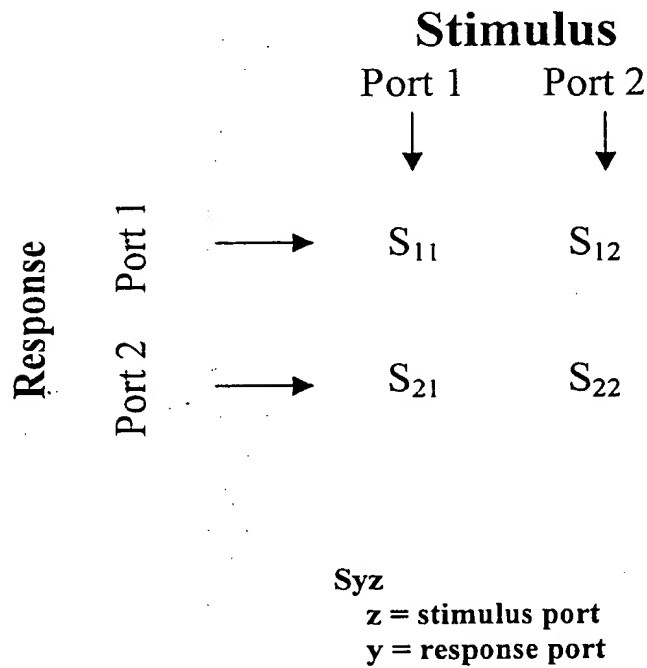


FIG. 1(a)

(Related Art)

THE MIXED-MODE S-MATRIX

		Stimulus			
		Differential Mode		Common Mode	
		Port 1	Port 2	Port 3	Port 4
Response	Differential Mode				
	Port 1	→ S_{DD11}	→ S_{DD12}	→ S_{DC11}	→ S_{DC12}
	Port 2	→ S_{DD21}	→ S_{DD22}	→ S_{DC21}	→ S_{DC22}
	Common Mode				
Common Mode	Port 3	→ S_{CD11}	→ S_{CD12}	→ S_{CC11}	→ S_{CC12}
	Port 4	→ S_{CD21}	→ S_{CD22}	→ S_{CC21}	→ S_{CC22}

S_{wxyz}

w = response mode

x = stimulus mode

z = stimulus port

y = response port

FIG. 1(b)

(Related Art)

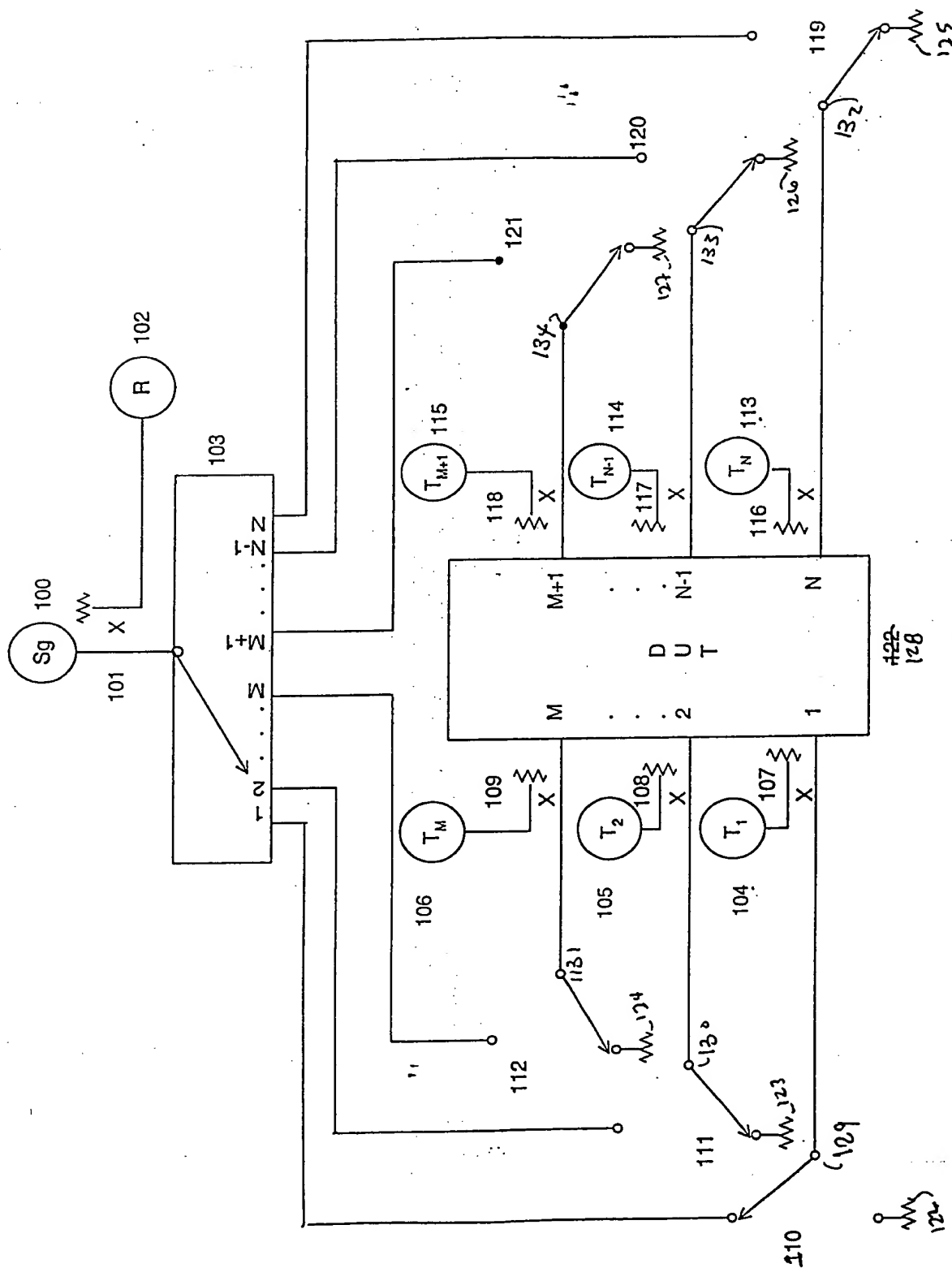


FIGURE 2

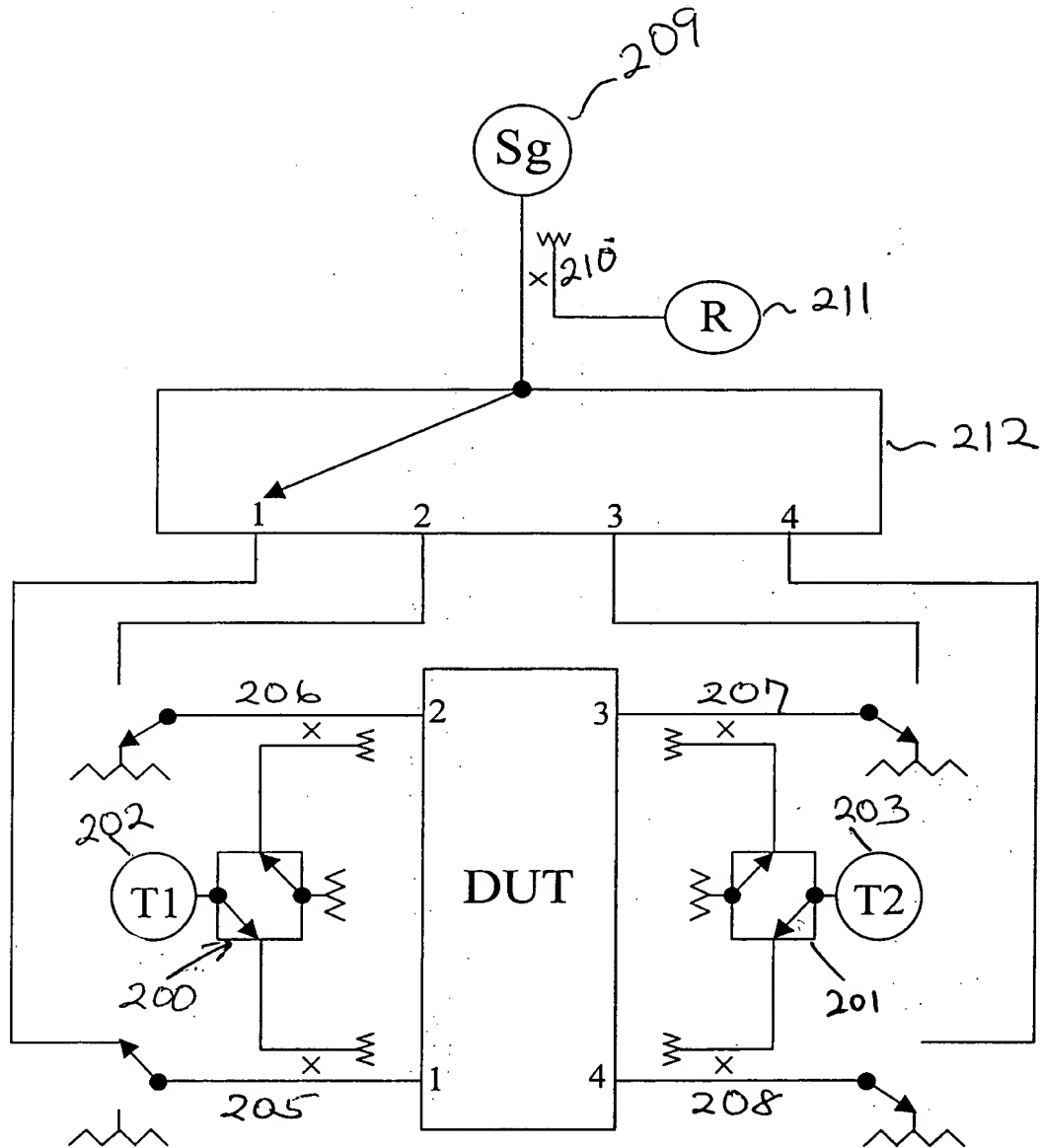


FIGURE 3

20

9-Port MTS Using 1 Reference & 3 Test Channel Receiver

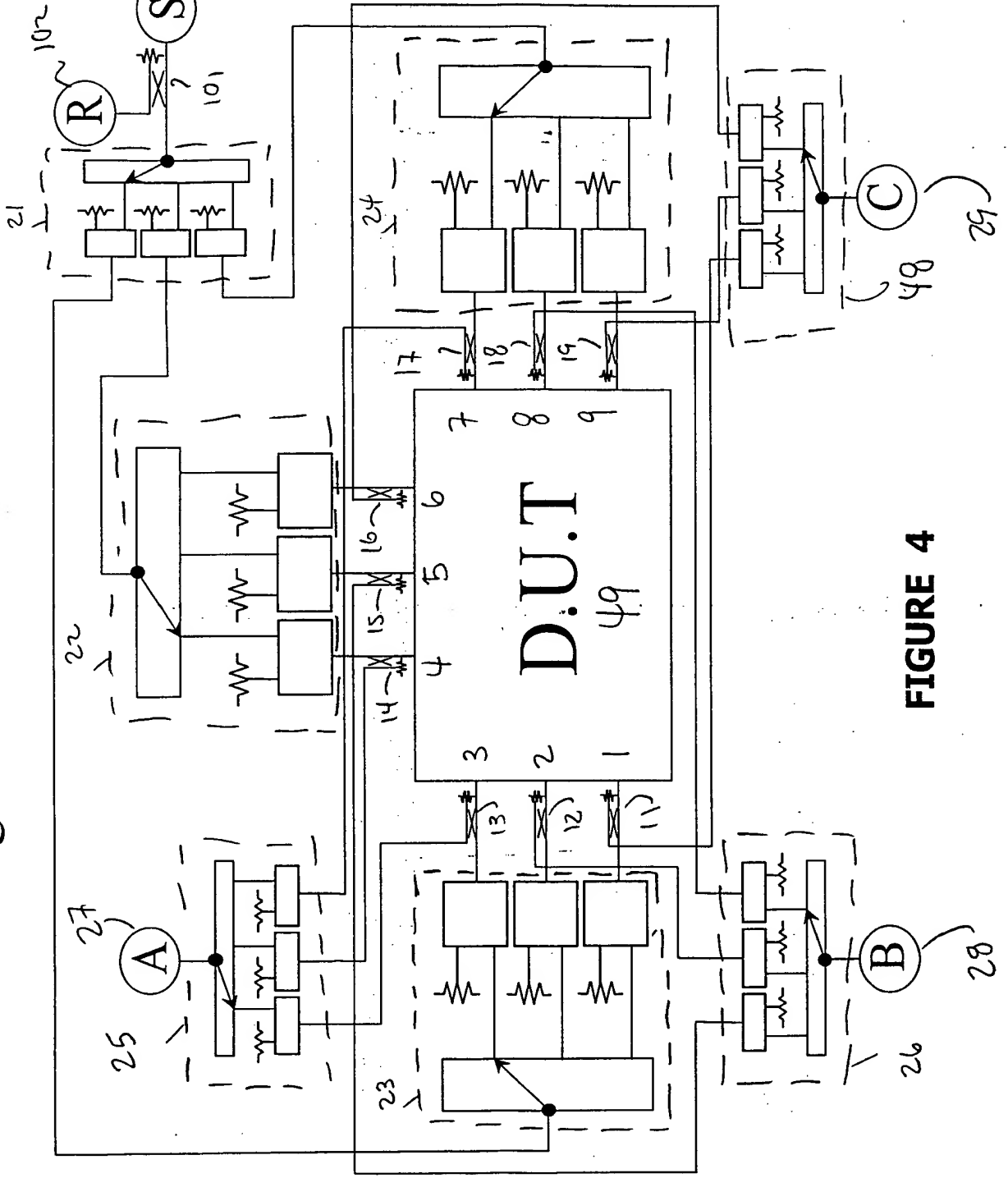


FIGURE 4

6-Port MTS Using 1 Reference & 2 Test Channel Receiver

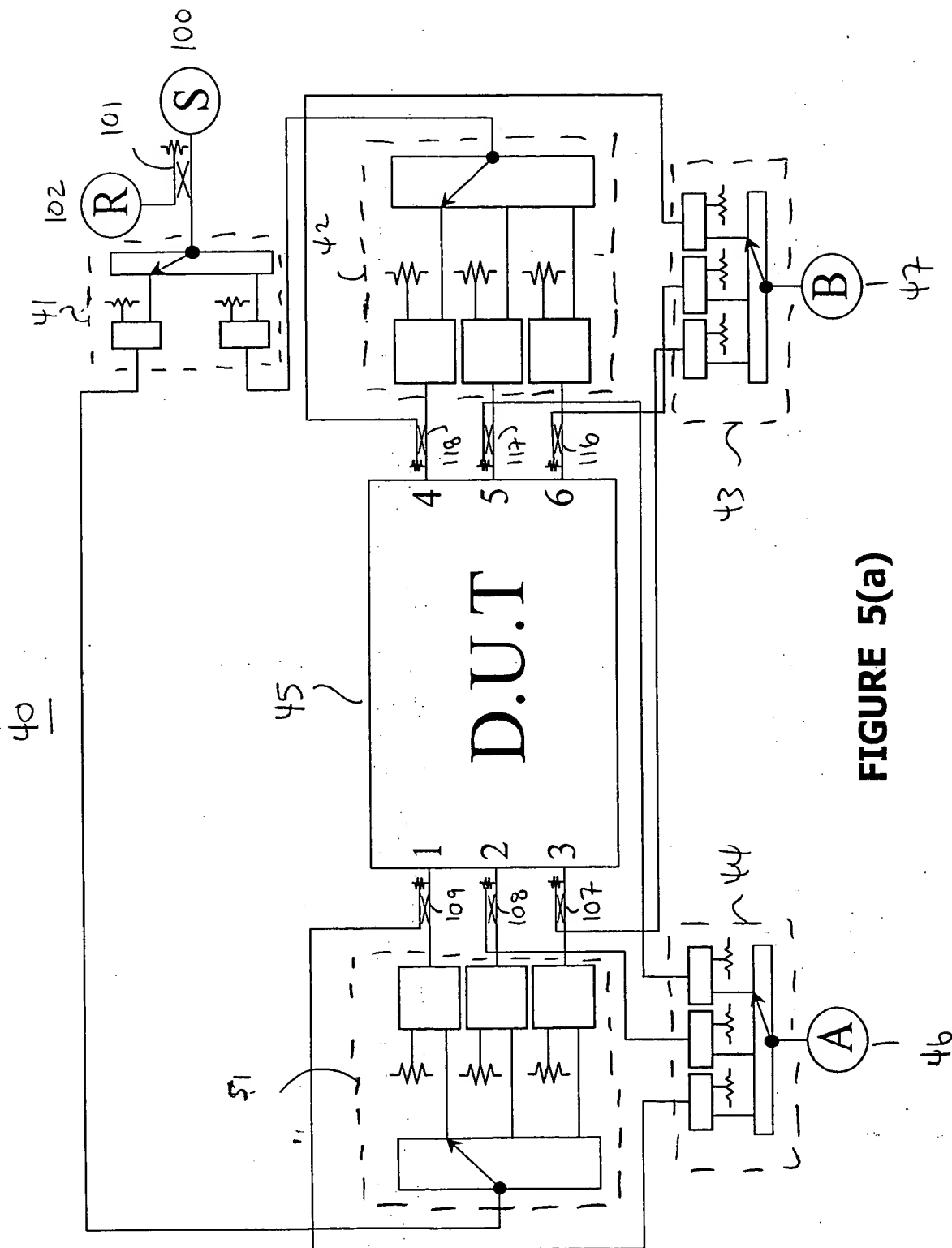


FIGURE 5(a)

6-Port MTS Using 1 Reference & 3 Test Channel Receiver

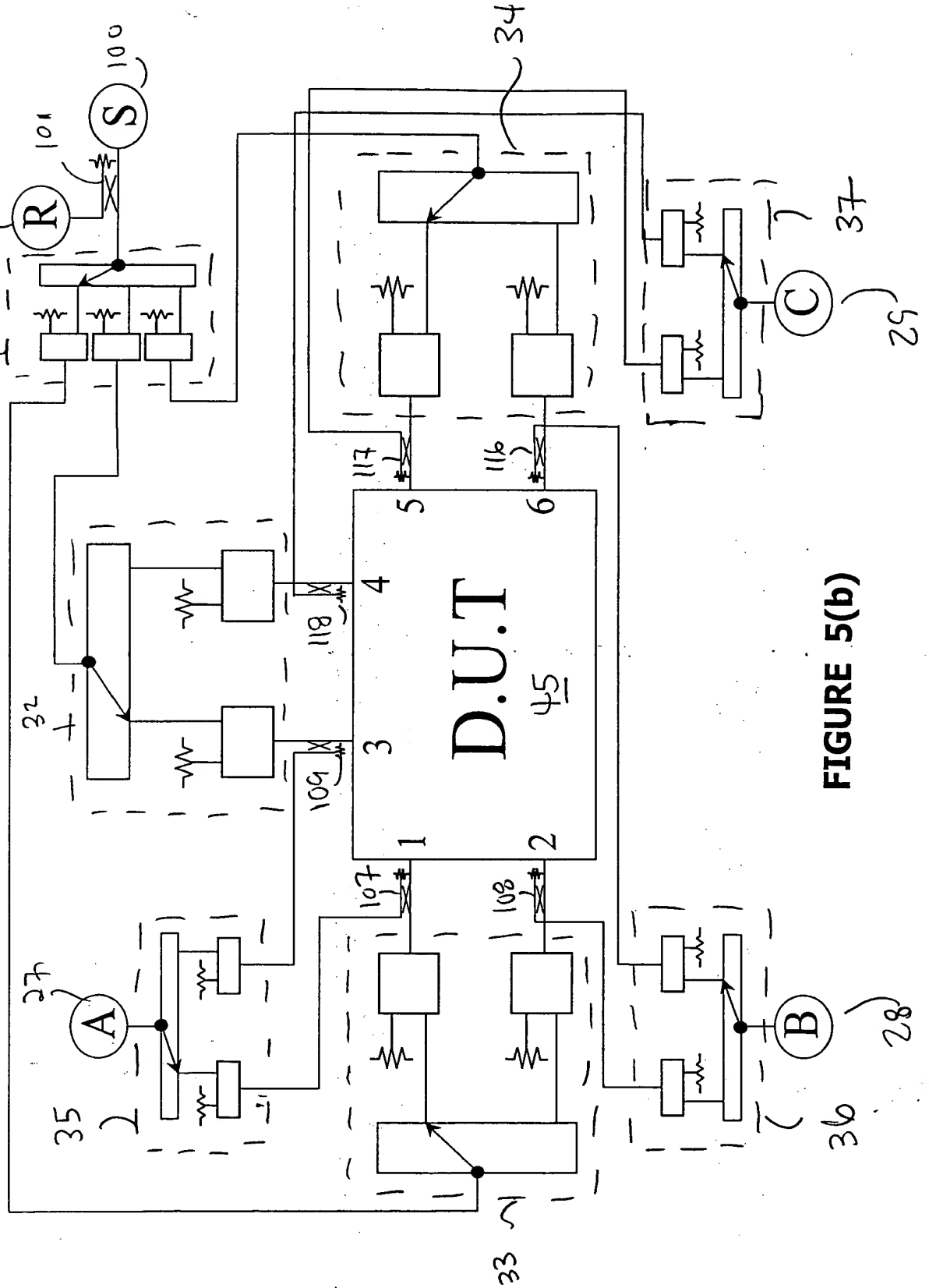


FIGURE 5(b)

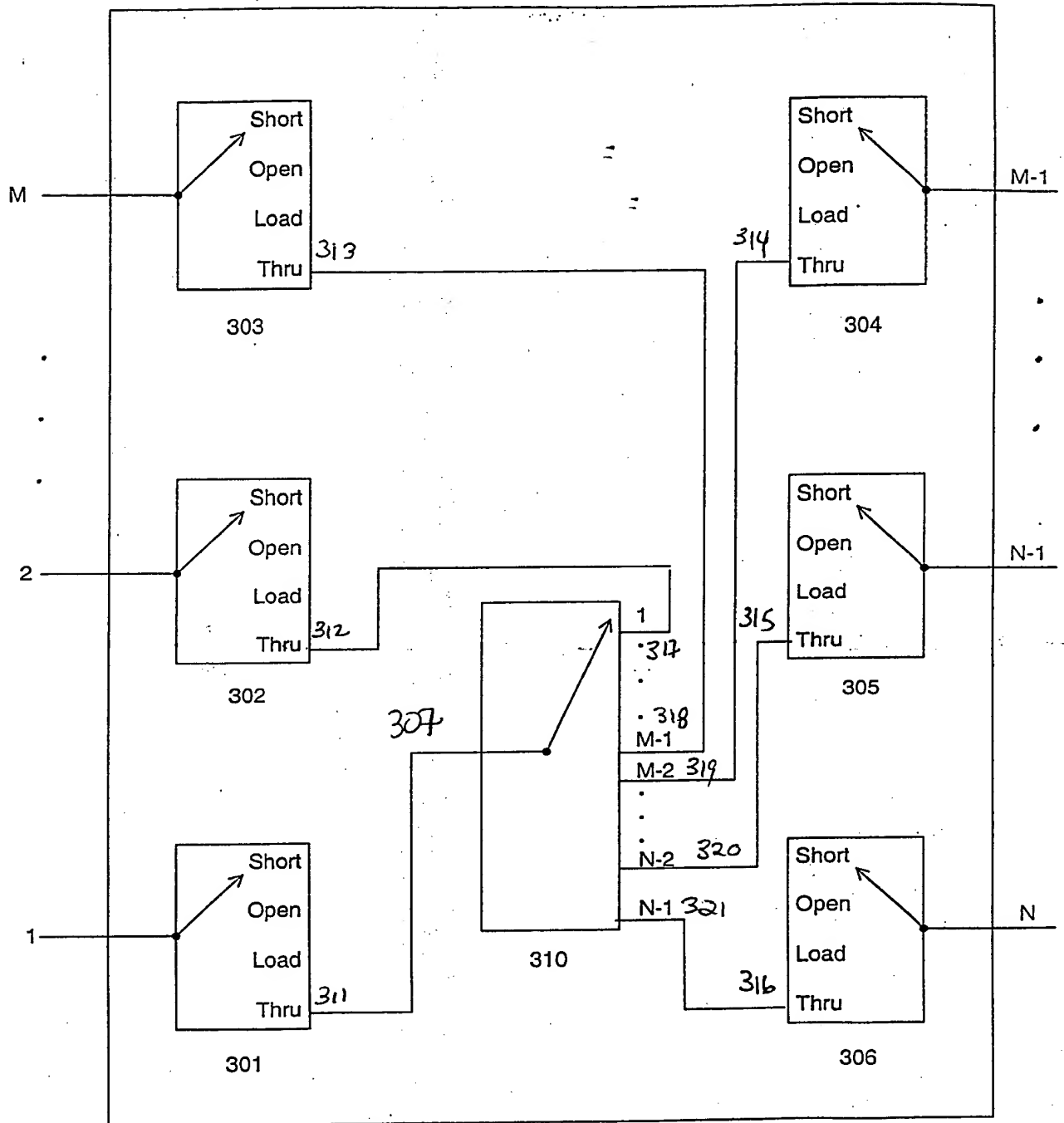


FIGURE 6
(Related Art)

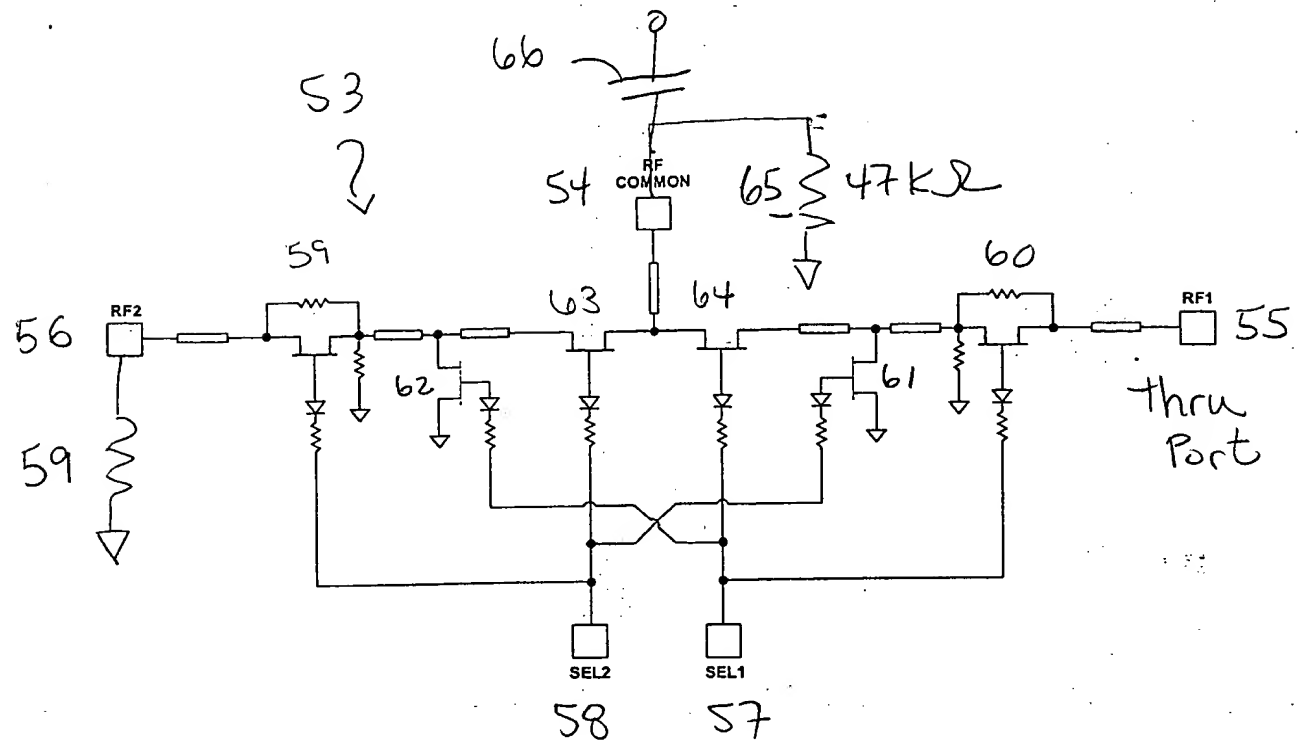


FIGURE 7

FET Switch Configurations

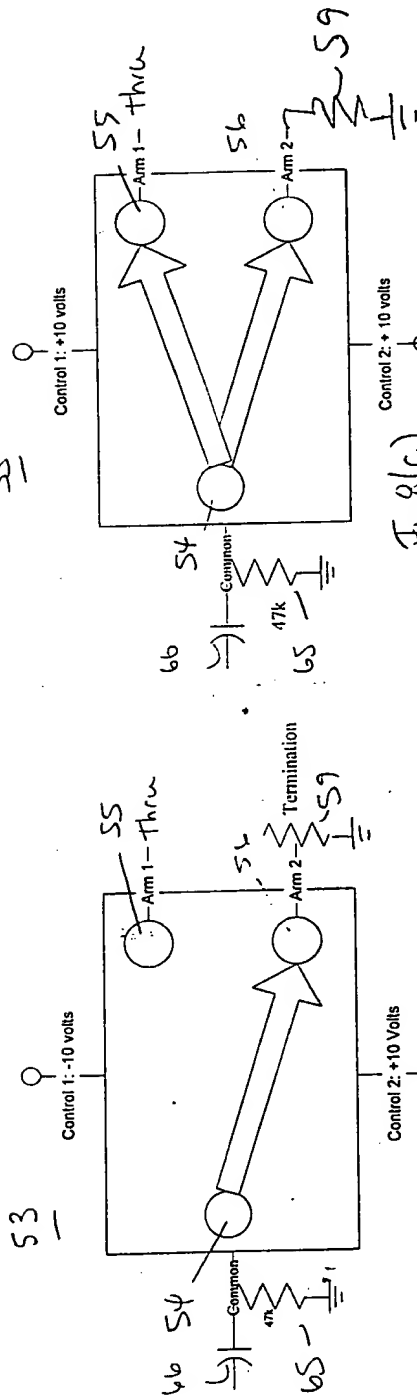


Figure 1
Common to Arm 2
Typical Termination State

Fig 8(c)

Figure 3
Common to Arm 1 & 2
Short Circuit

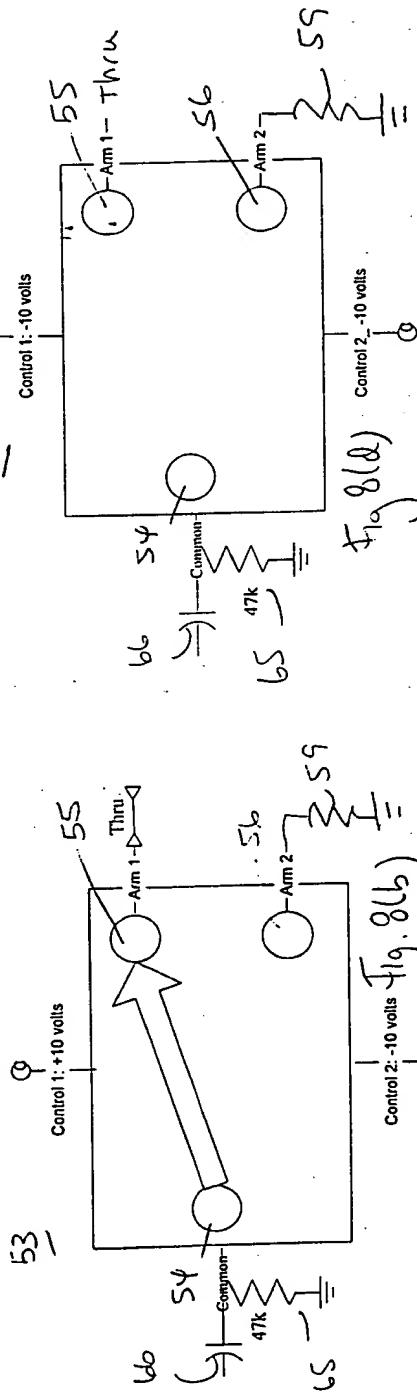
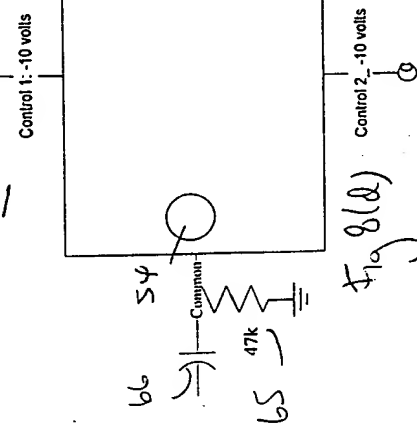


Figure 2
Common to Arm 1
Thru condition

FIGURE 8

Figure 4
Both Arms off
Open Circuit



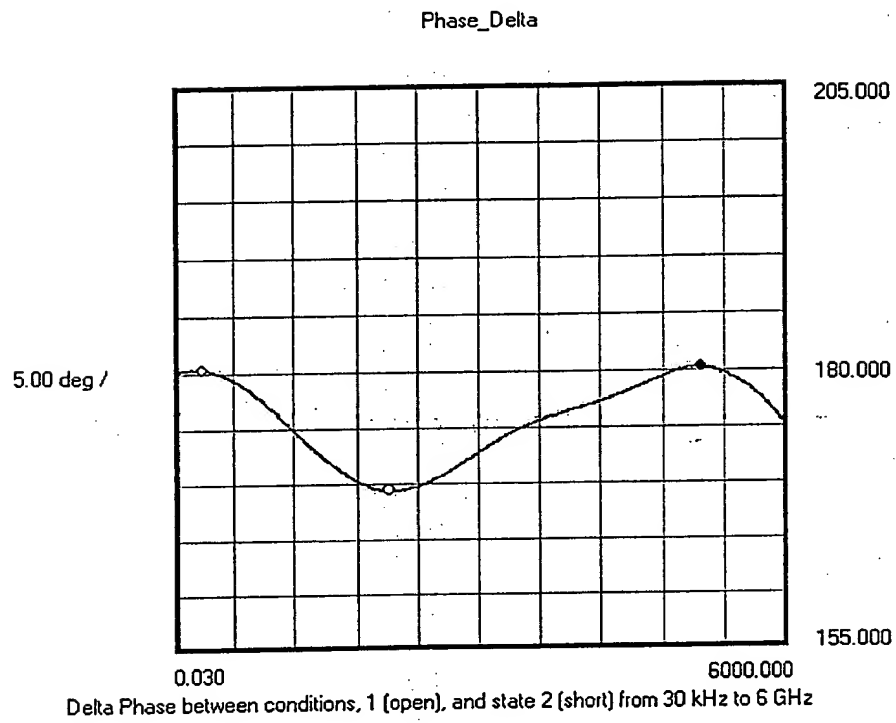


FIGURE 9

S22

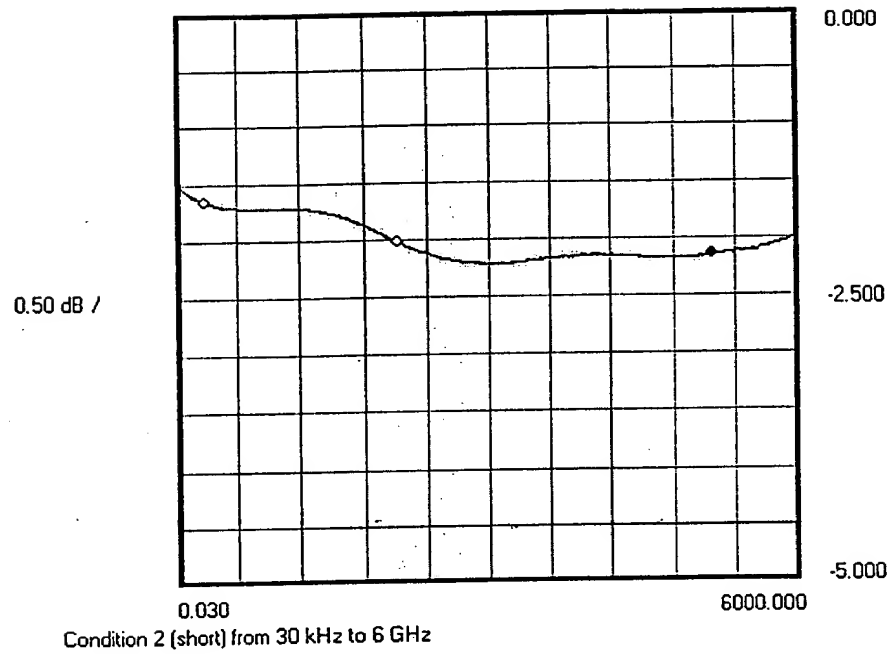


FIGURE 10

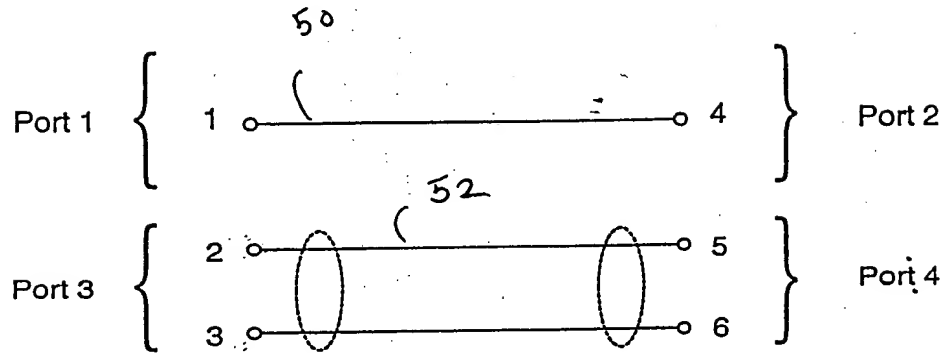


FIGURE 11

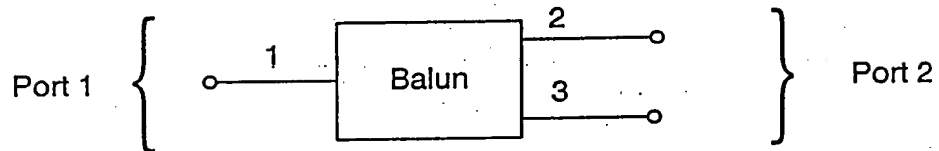


FIGURE 13

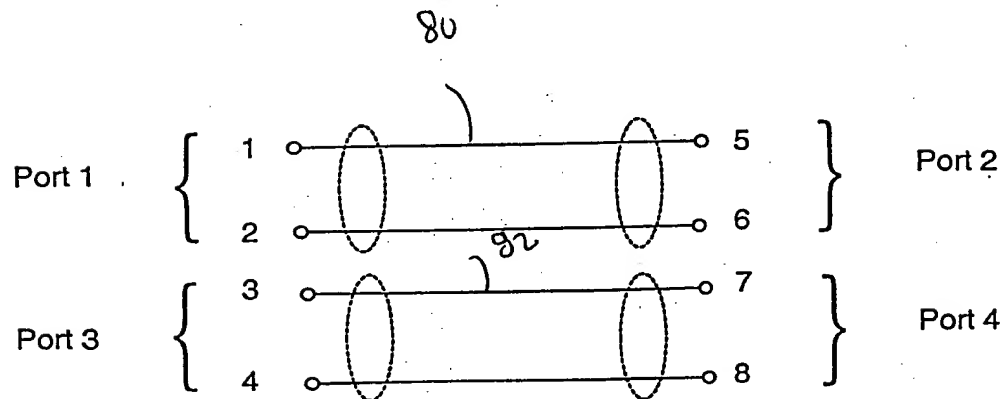


FIGURE 14

"000000" 69849660



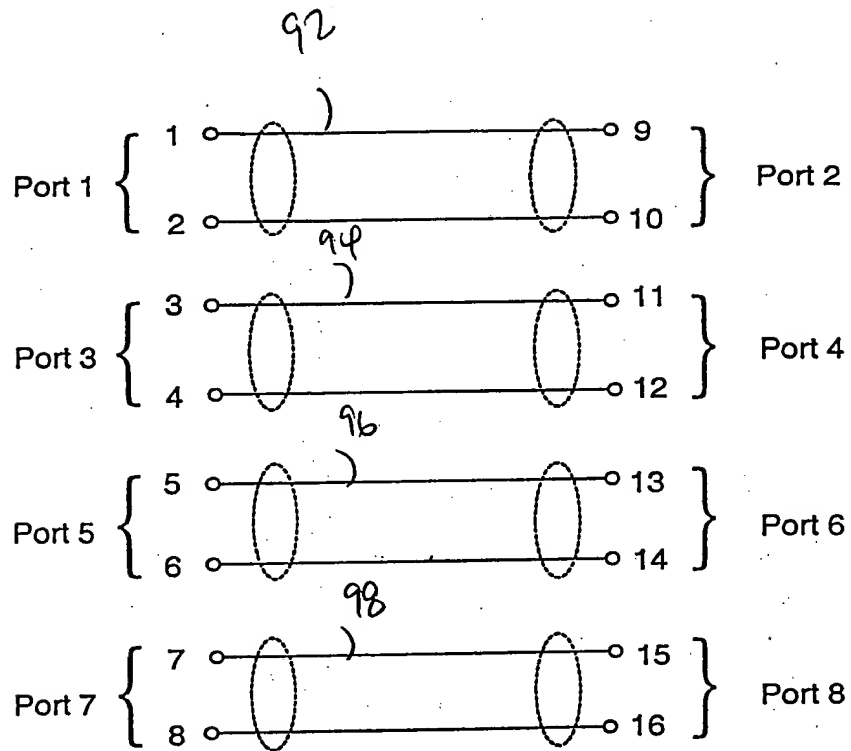


FIGURE 15

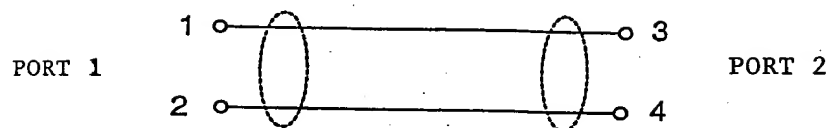


FIGURE 16

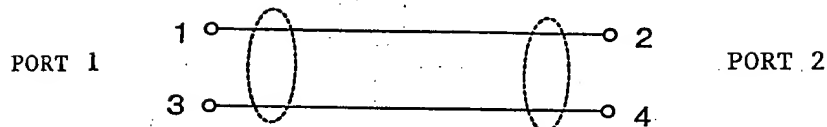


FIGURE 17

Tested: 00245650

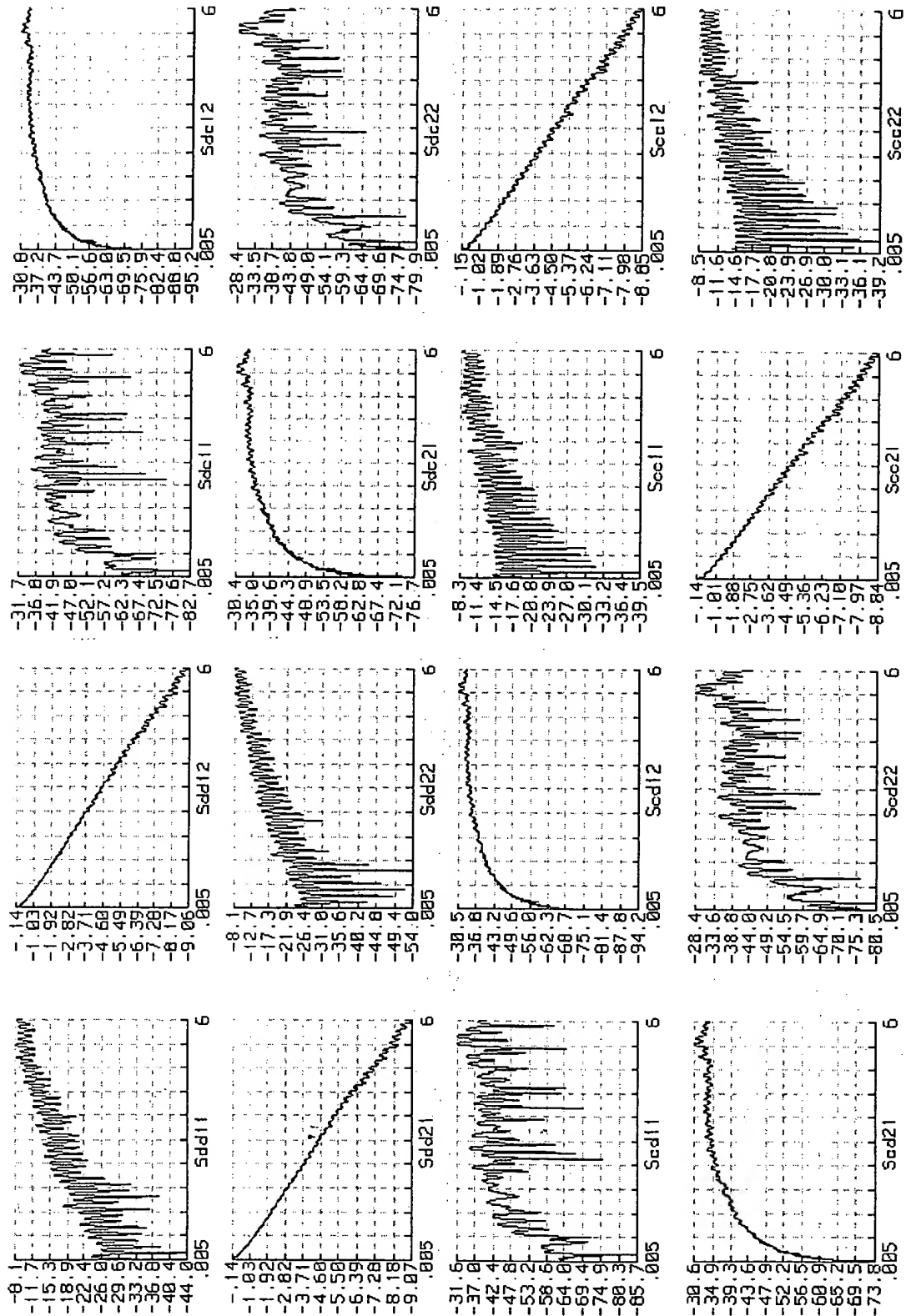


FIGURE 18

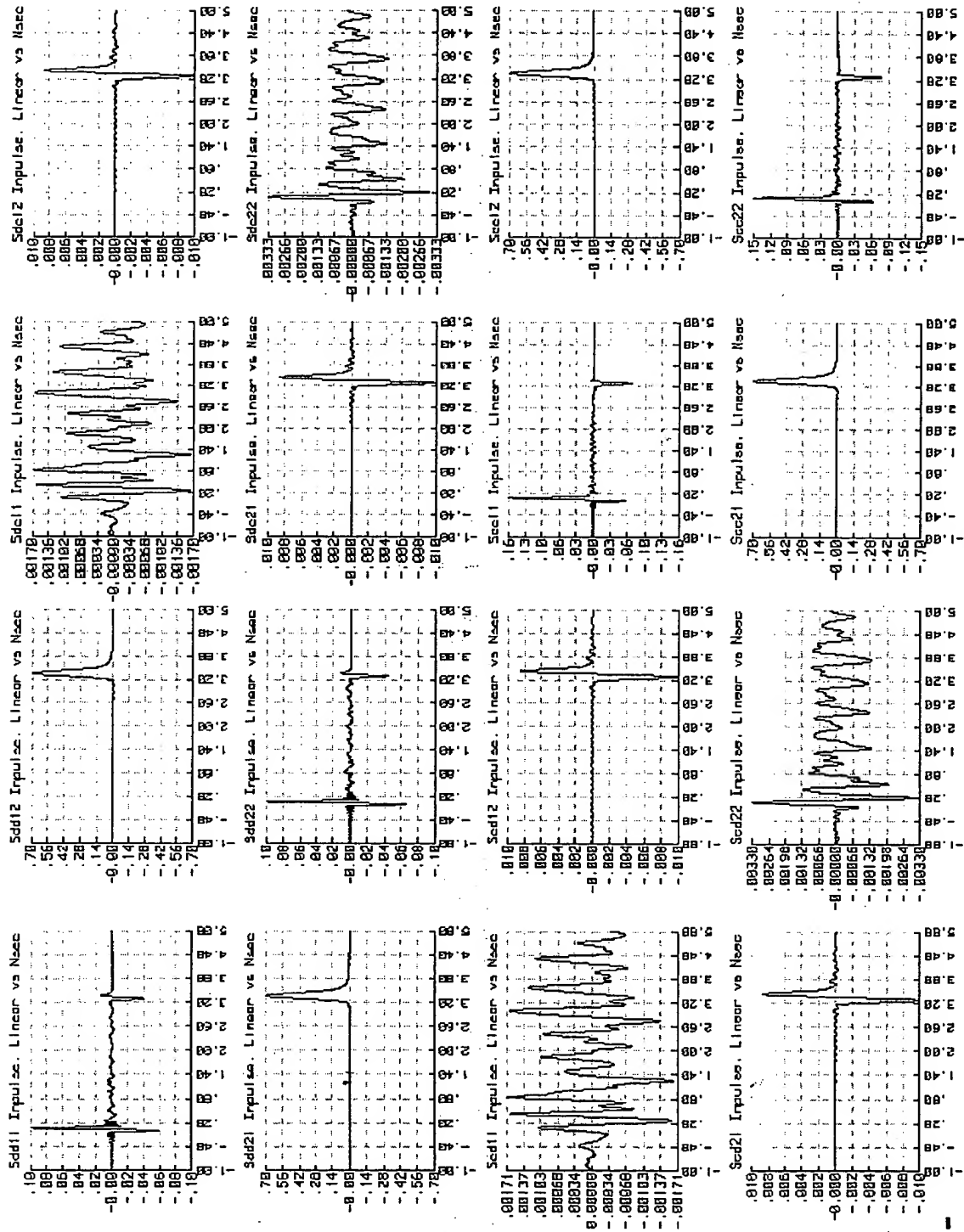


FIGURE 19

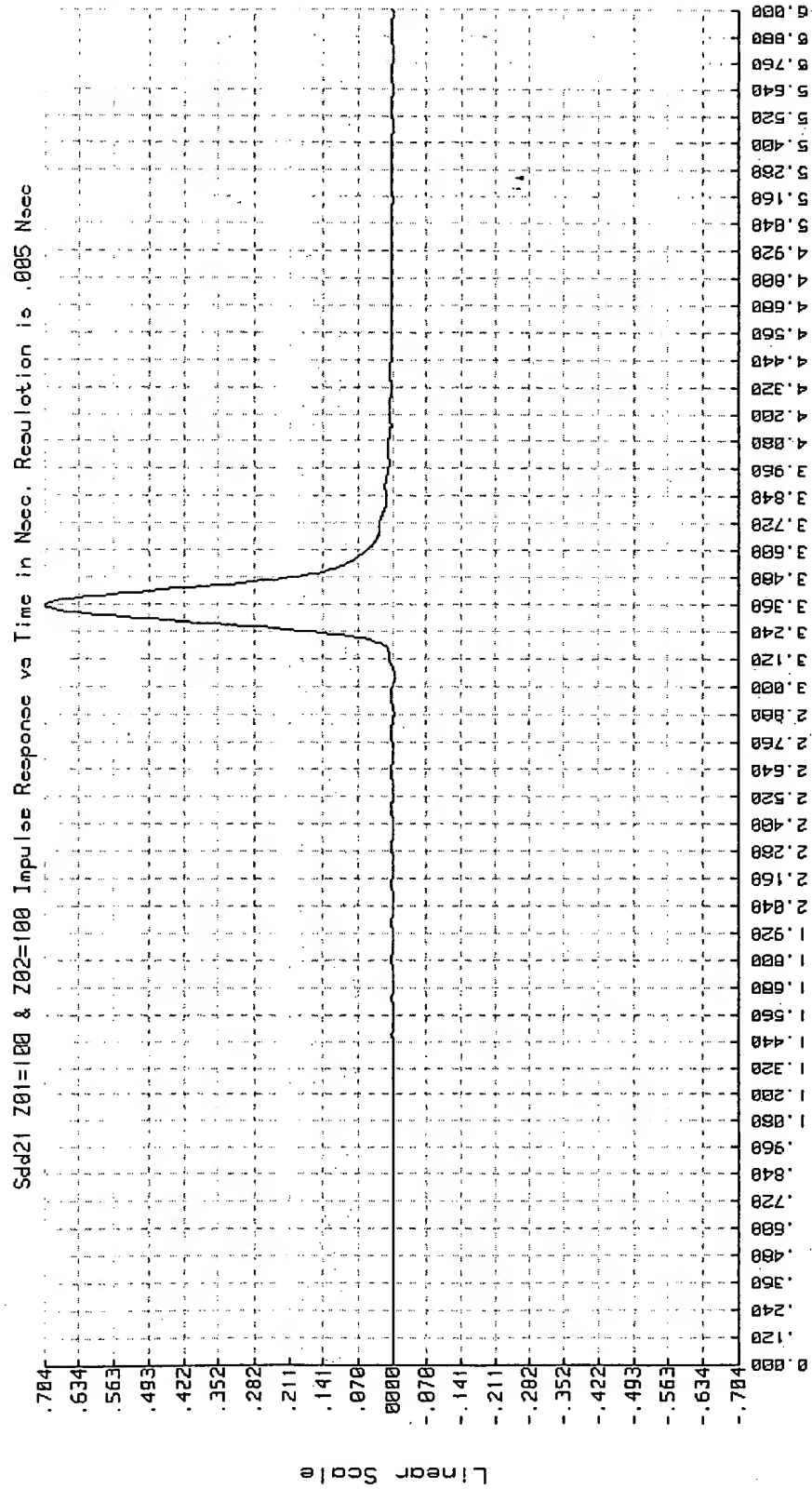


FIGURE 20

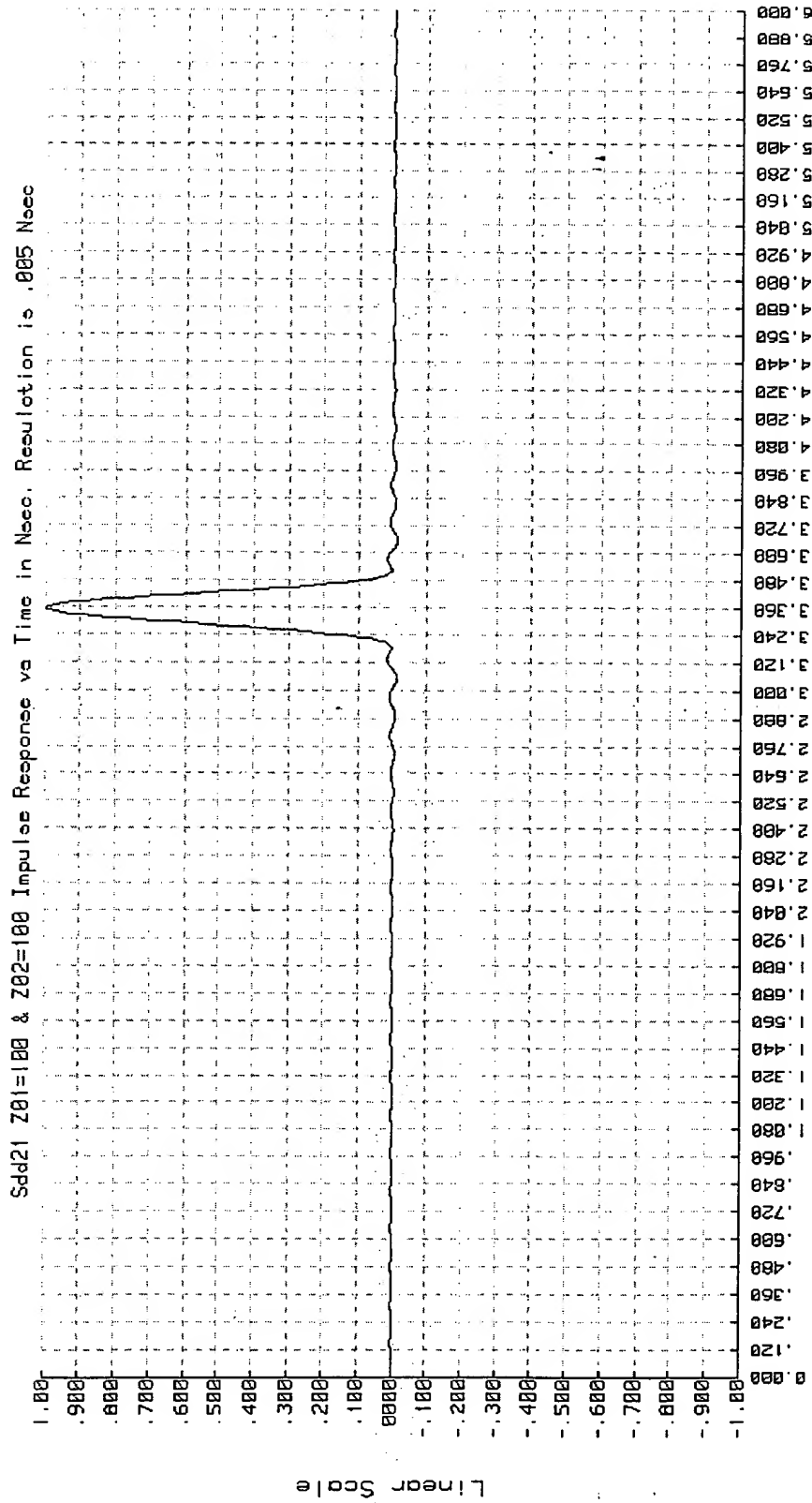


FIGURE 21

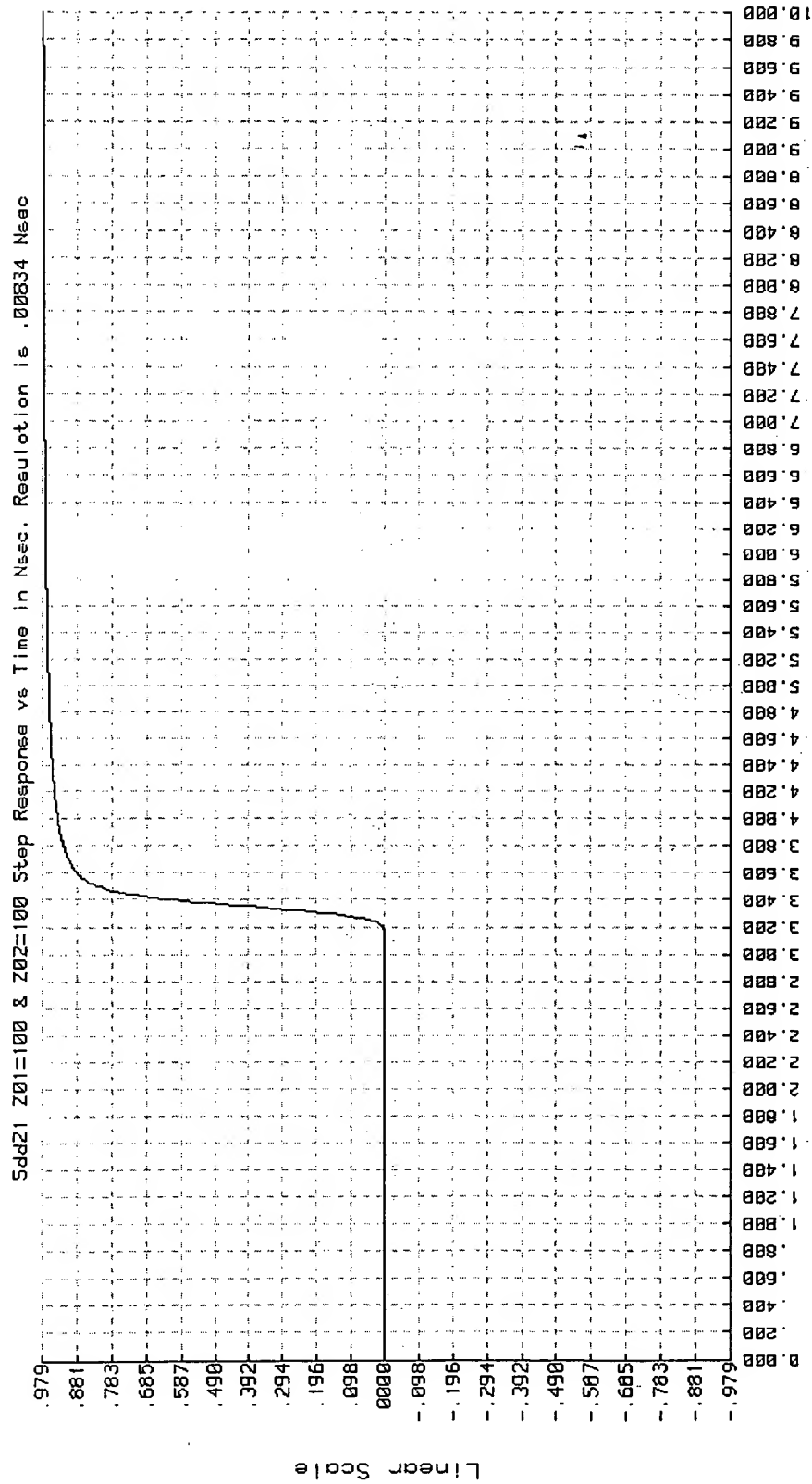


FIGURE 22

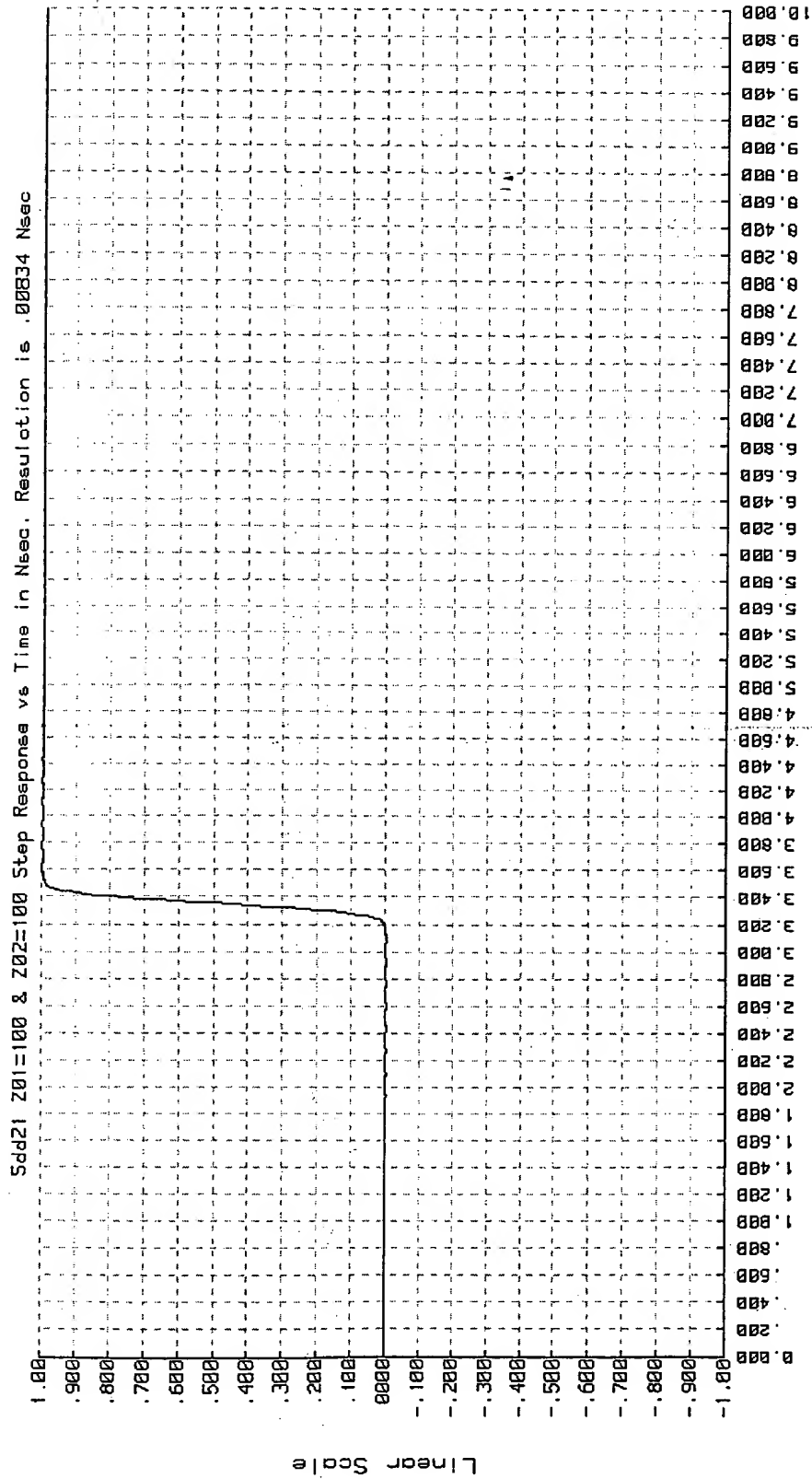
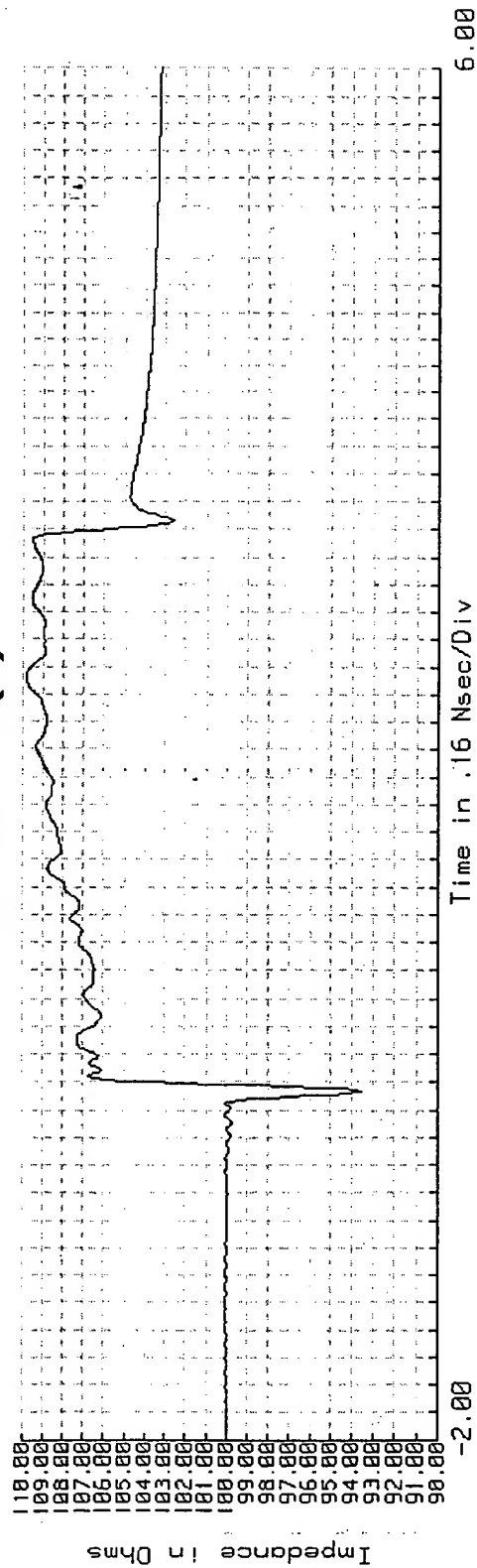
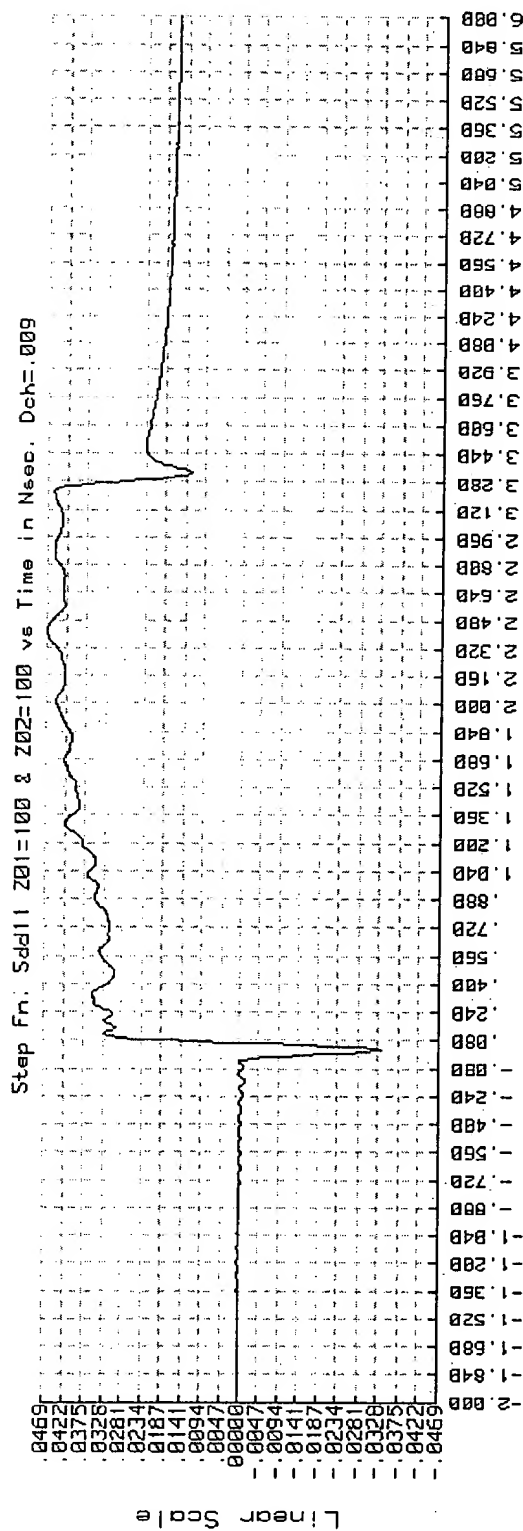


FIGURE 23



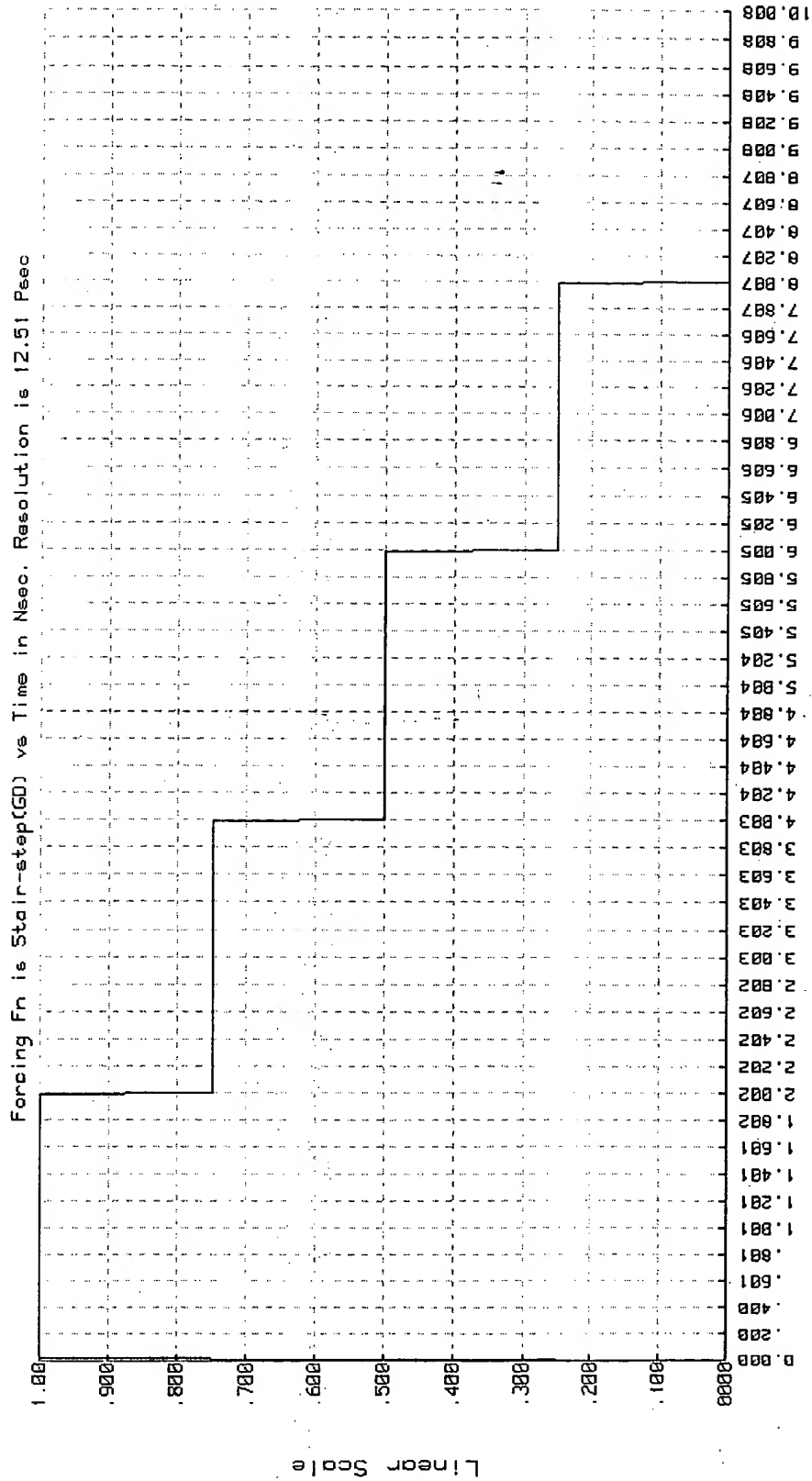


FIGURE 25

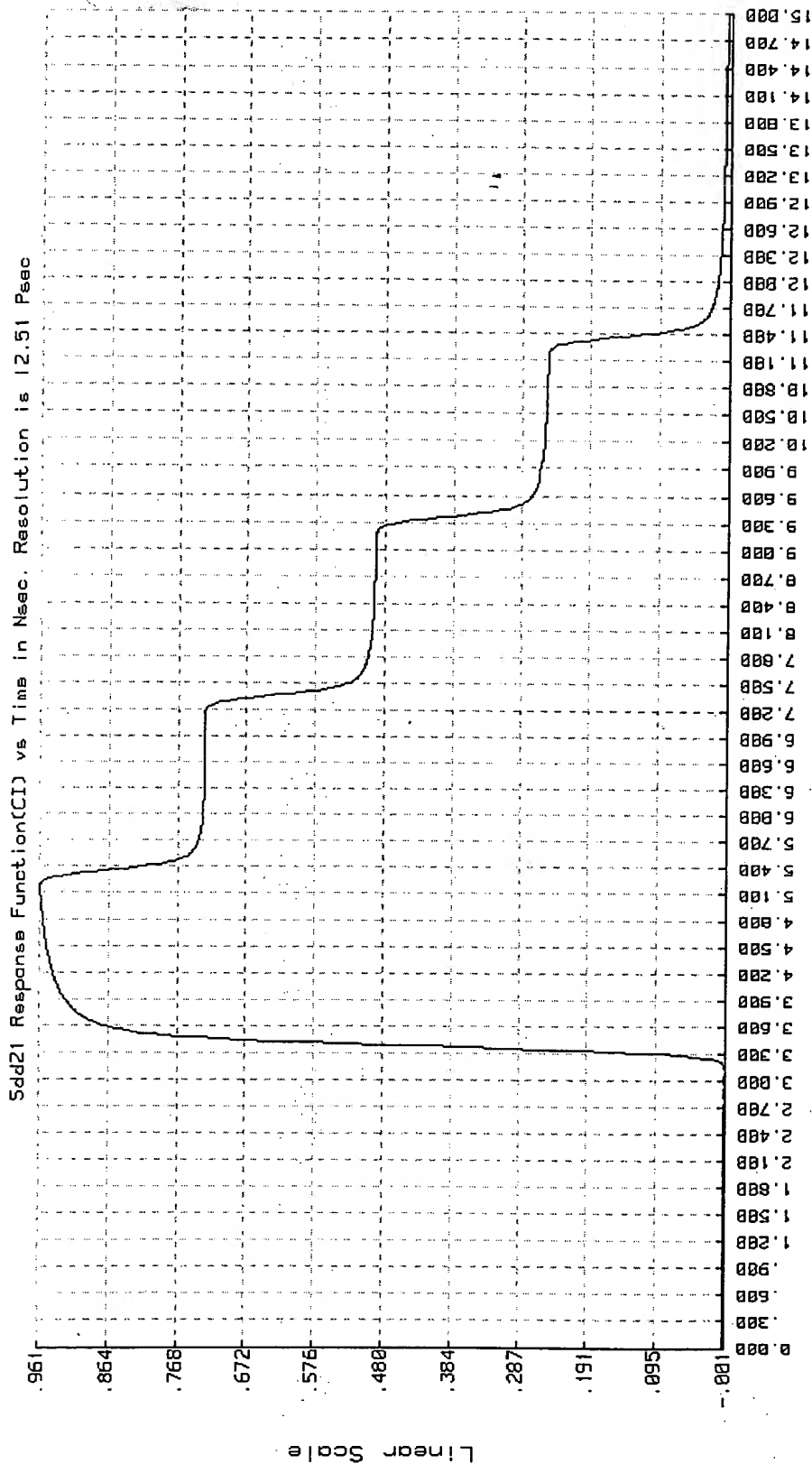


FIGURE 26

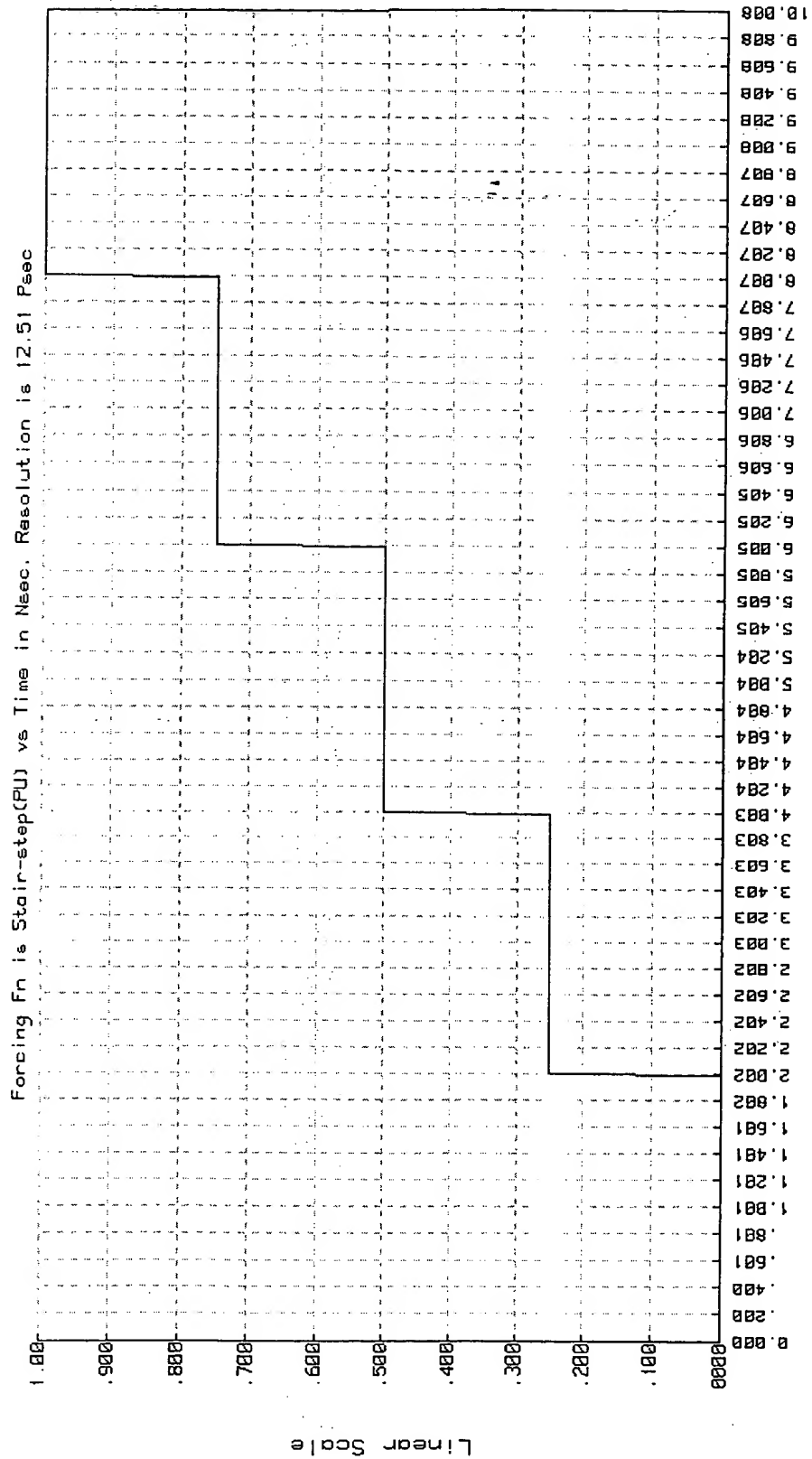


FIGURE 27

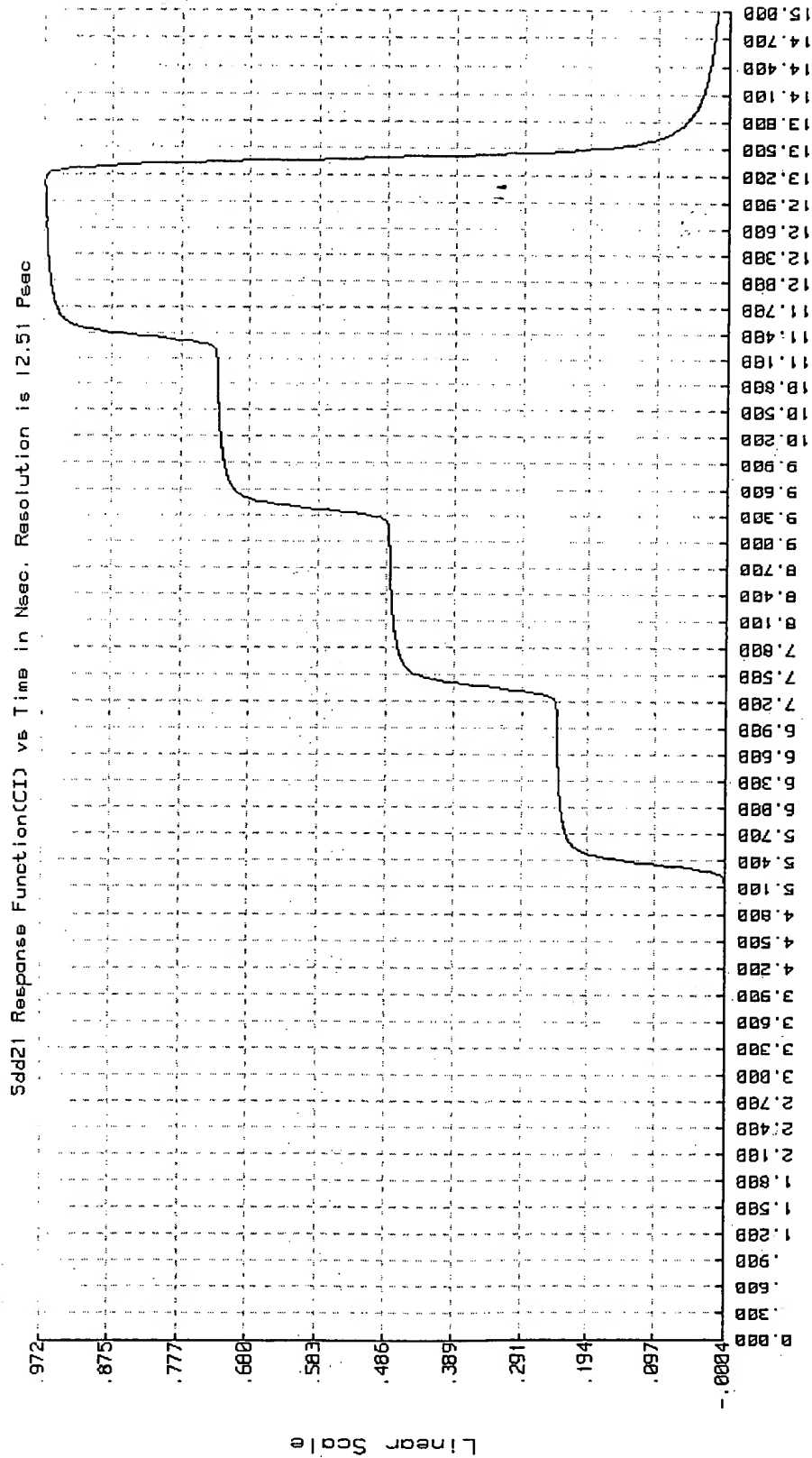


FIGURE 28

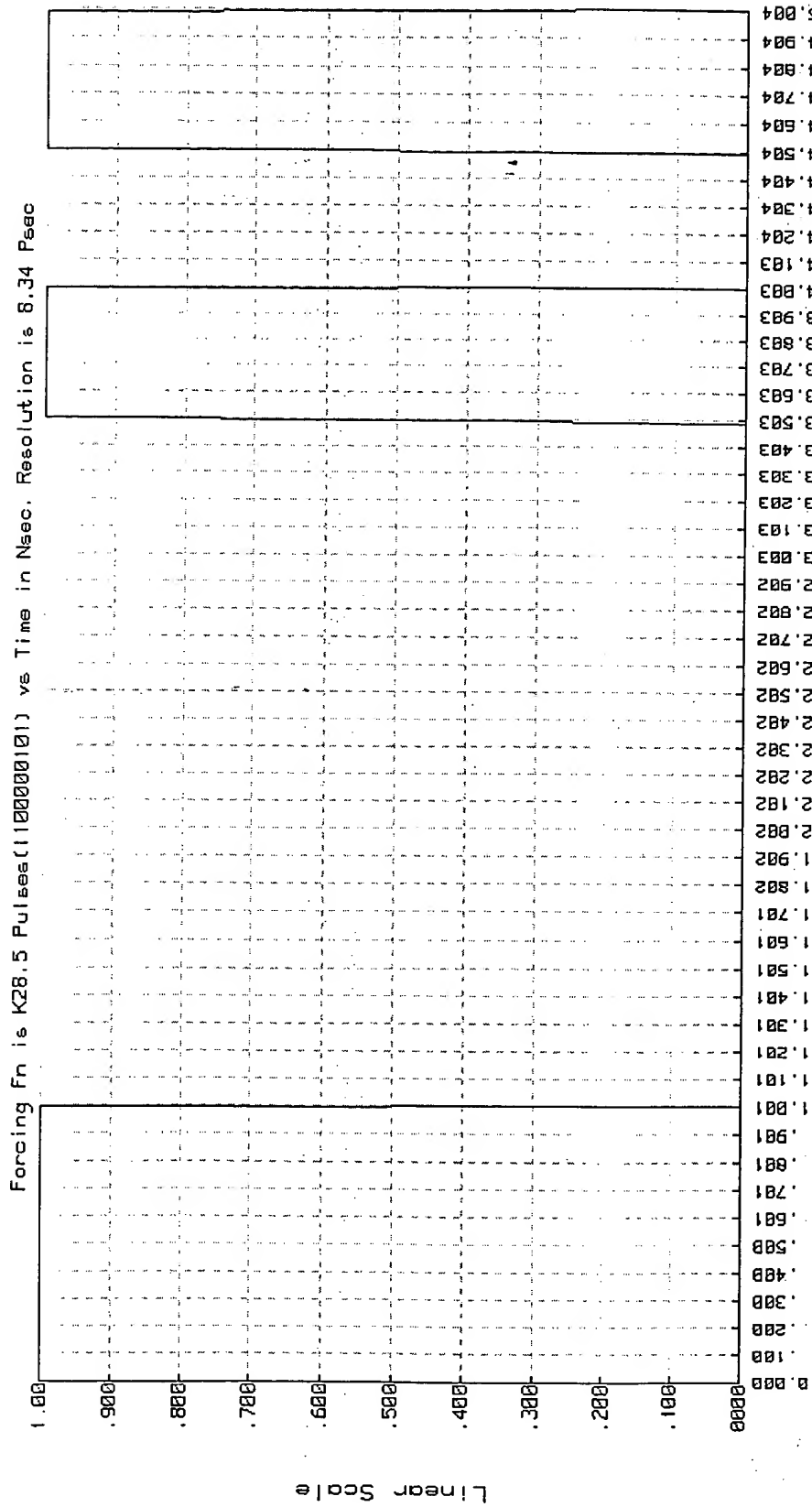


FIGURE 29

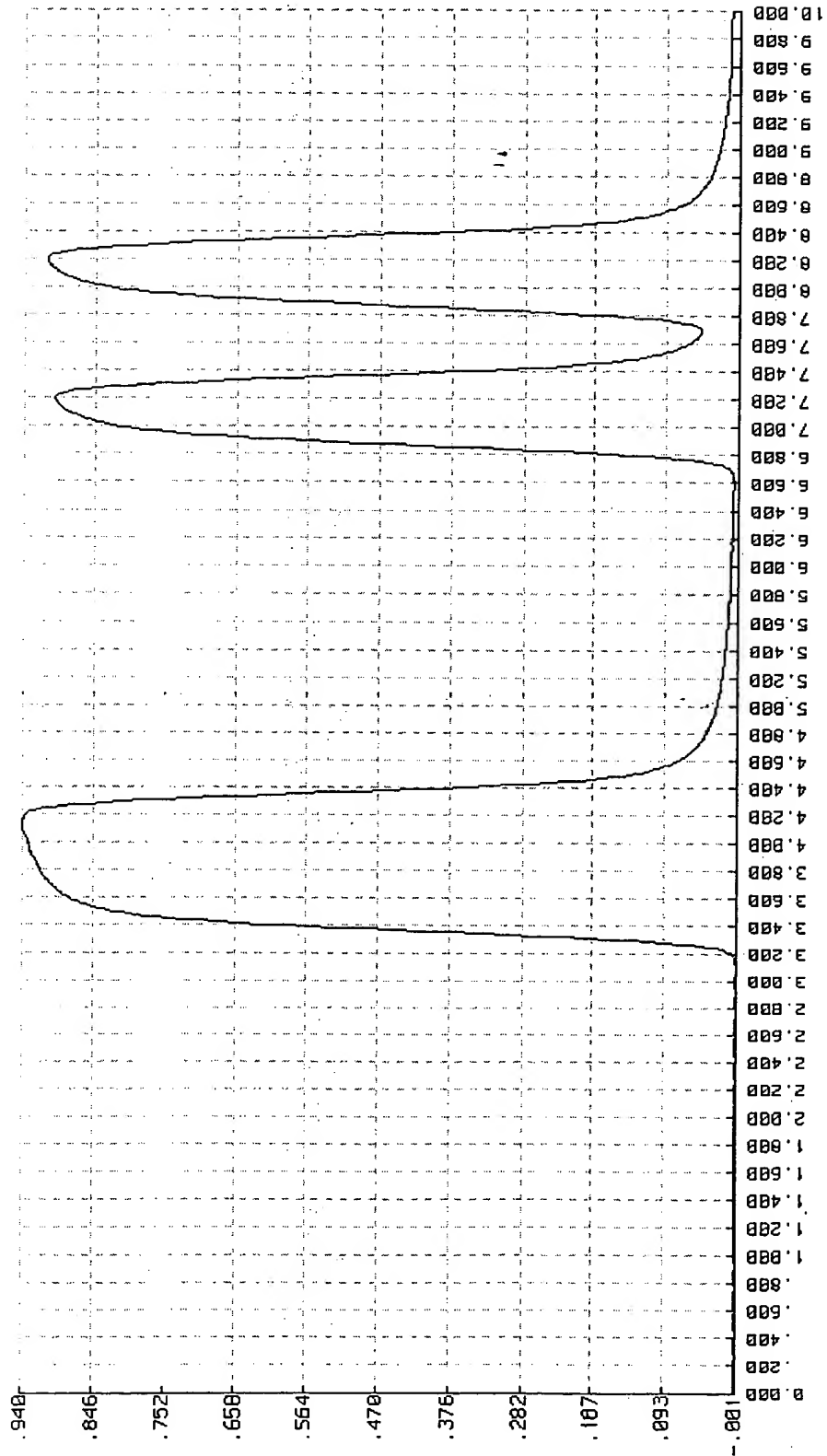


FIGURE 30

102150 000462

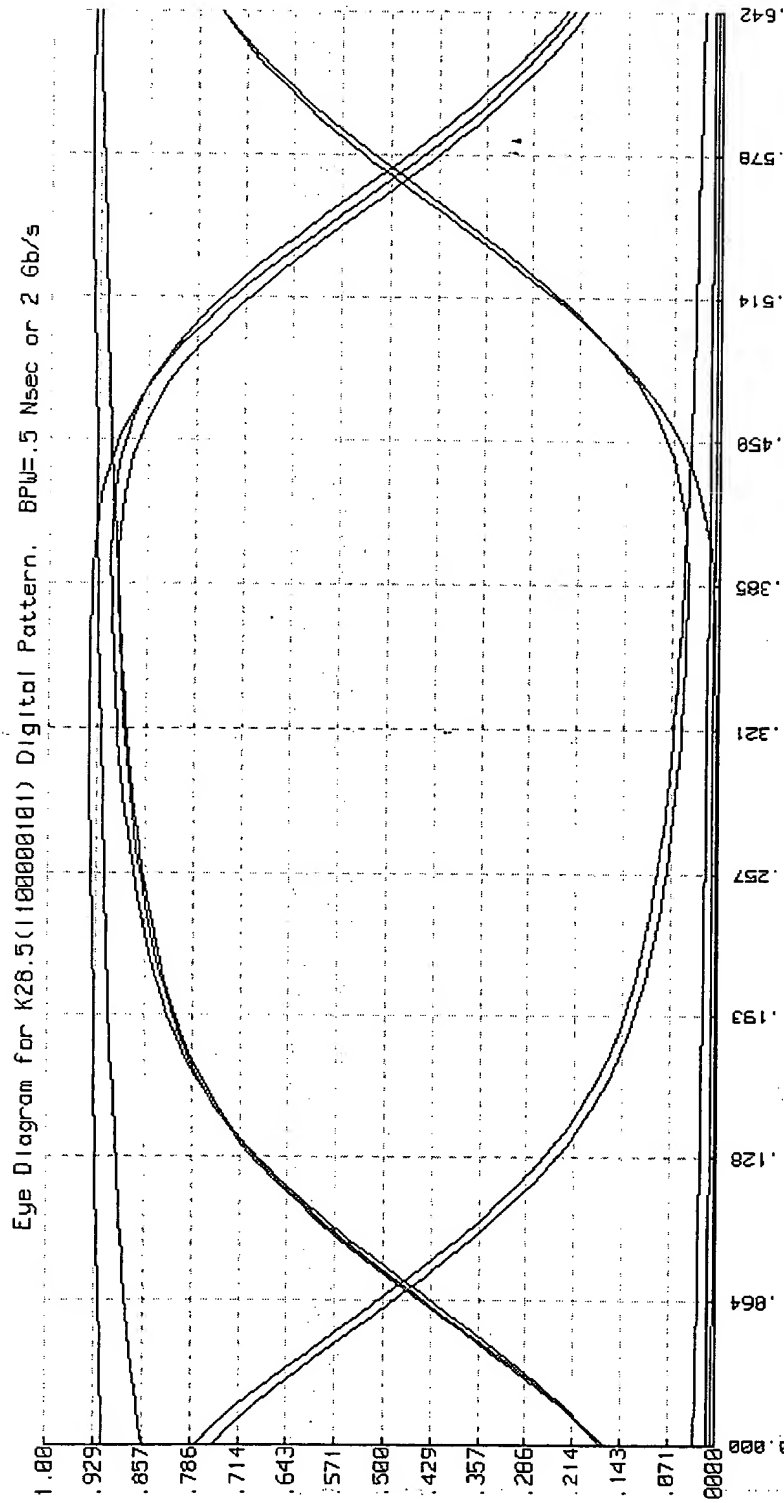


FIGURE 31

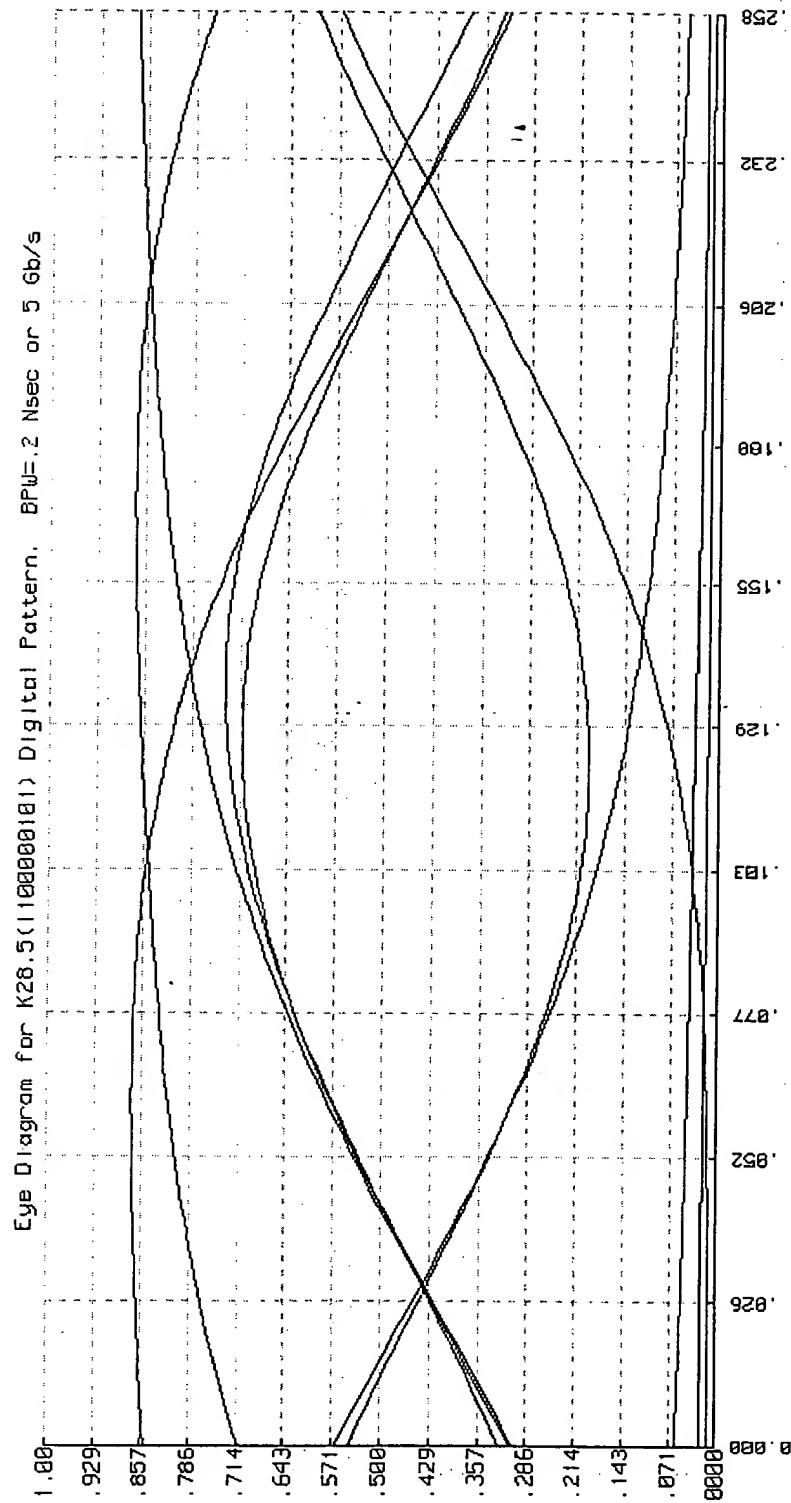


FIGURE 32

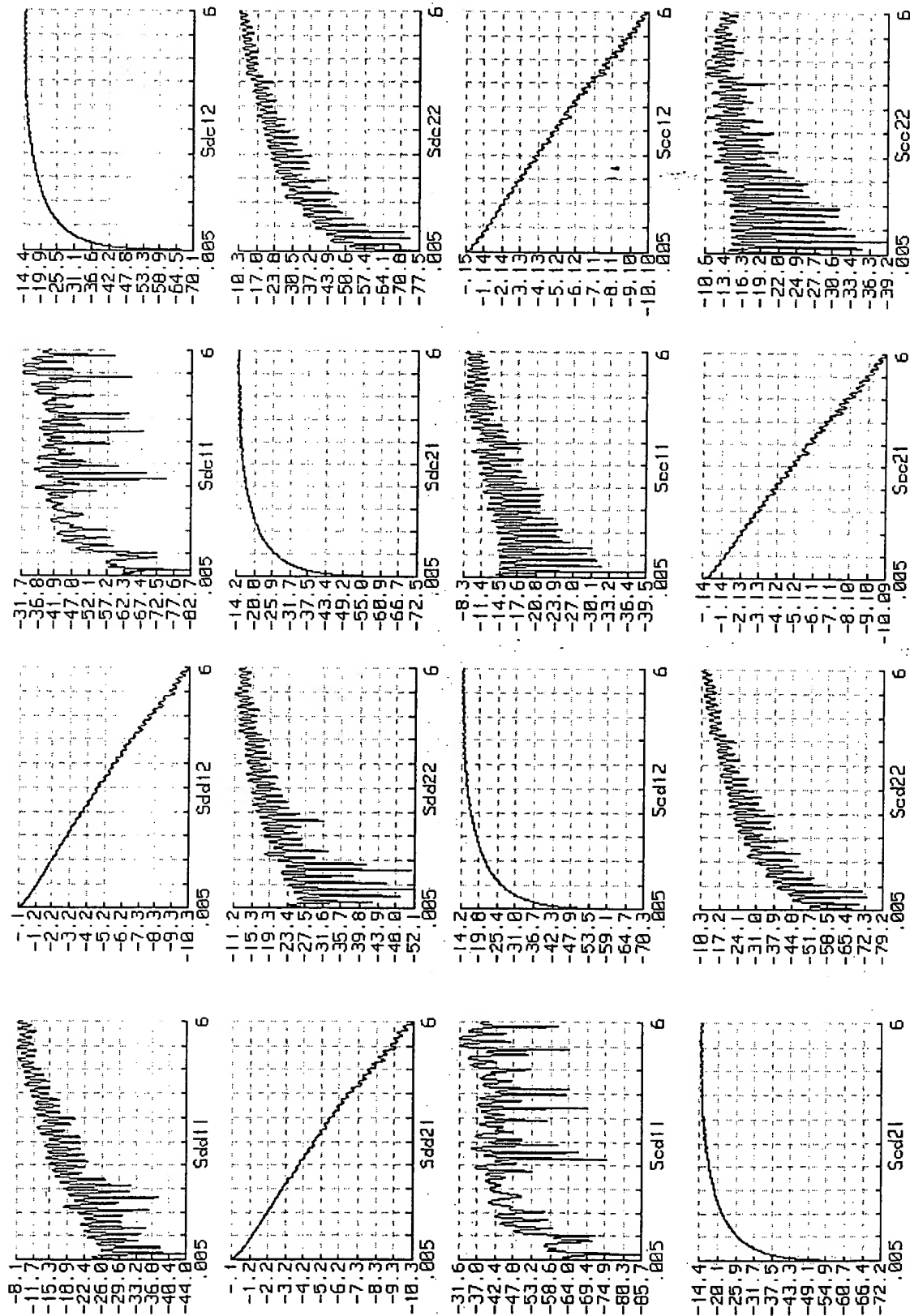


FIGURE 33

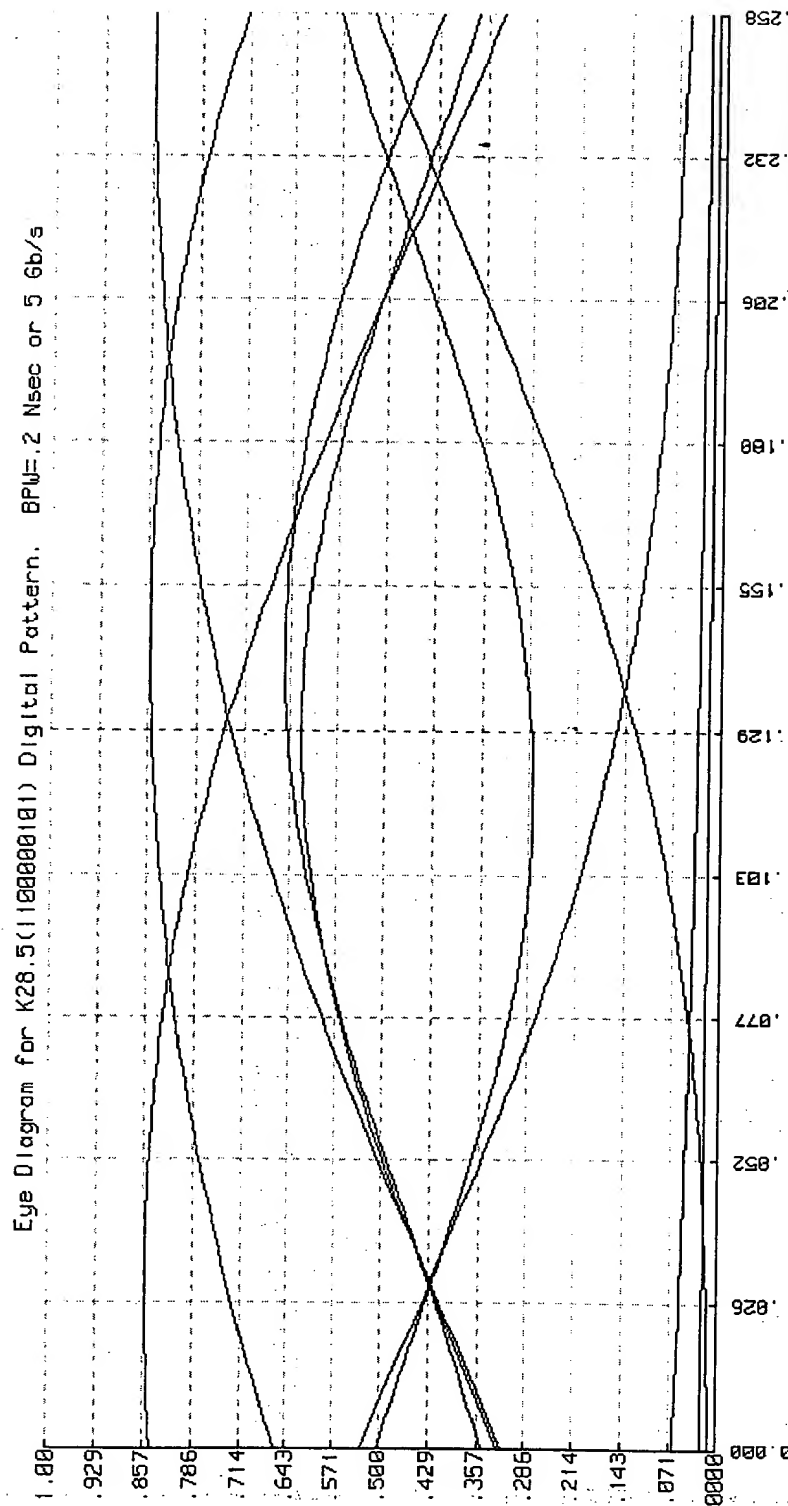


FIGURE 34